

Appl. No. 09/818,062
Amdt. dated 02/01/2005
Reply to Office action of 09/01/2004

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (original):

In a network device including a host system coupled to a memory to store data and a line card to interface with a plurality of user devices, a method comprising:

receiving a network packet including voice data by the host system;

storing the voice data in the memory;

sending a voice packet related to the voice data to the line card without duplication, the voice packet including descriptor fields for multicasting the voice data; and

selectively multicasting the voice data stored in the memory to the plurality of user devices based on the descriptor fields in the voice packet.

2. (original):

The method of claim 1, wherein the receiving of the network packet includes receiving an Internet Protocol (IP) packet having the voice data.

3. (original):

The method of claim 1, wherein the descriptor fields include a memory pointer field, status field, mask field, or a data length field.

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4. (original):

The method of claim 3, wherein the selectively multicasting of the voice data includes using a multicast hardware accelerator to send the voice data to selected user devices based on the mask field.

5. (original):

The method of claim 4, wherein the multicast hardware accelerator includes a field programmable gate array (FPGA) device.

6. (original):

A digital processing system comprising:

a host system to receive a network packet including voice data;

a memory to store the voice data; and

a line card to receive a voice packet related to the voice data without duplication, the voice packet including descriptor fields for multicasting the voice data, and to multicast selectively the voice data stored in the memory to the plurality of user devices based on the descriptor fields in the voice packet.

7. (original):

The digital processing system of claim 6, wherein the host system is to receive an Internet Protocol (IP) packet having the voice data.

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8. (original):

The digital processing system of claim 6, wherein the descriptor fields include a memory pointer field, status field, mask field, or a data length field.

9. (original):

The digital processing system of claim 8, wherein the line card includes a multicast hardware accelerator to send the voice data to selected user devices based on the mask field.

10. (original):

The digital processing system of claim 9, wherein the multicast hardware accelerator includes a field programmable gate array (FPGA) device.

11. (original):

An apparatus comprising:

means for receiving a network packet including voice data;

means for storing the voice data;

means for receiving the voice packet related to the voice data without duplication, the voice packet including descriptor fields for multicasting the voice data; and

means for multicasting selectively the stored voice data to a plurality of user devices based on the descriptor fields in the voice packet.

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12. (original):

The apparatus of claim 11, wherein the means for receiving the network packet includes means for receiving an Internet Protocol (IP) packet having the voice data.

13. (original):

The apparatus of claim 11, wherein the descriptor fields include a memory pointer field, status field, mask field, or a data length field.

14. (original):

The apparatus of claim 13, wherein the means for multicasting includes means for accelerating the multicasting of the voice data based on the mask field.

15. (original):

The digital processing system of claim 11, wherein the user devices include digital signal processing devices.

16. (currently amended):

A network device comprising:

a host system including a host central processing unit (CPU) and an operating the system, the host system to process packets a network packet including voice data received from a network;

a buffer memory coupled to the host system, the buffer memory adapted to store voice data extracted from the network packet ~~processed packets~~ by the host system; and

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a line card coupled to the host system and the buffer memory, the line card having a plurality of ports to interface to user devices, the line card including a multicast hardware accelerator to multicast the voice data stored in the buffer memory to ports selected from the plurality of ports by a single voice packet received from the host system. ~~memory.~~

17. (original):

The network device of claim 16, wherein the multicast hardware accelerator includes a field programmable gate array (FPGA) device.

18. (currently amended):

The network device of claim 16, wherein ~~the host system is to send a packet relating to the data stored in the buffer memory,~~ the voice packet includes descriptor fields used to multicast the voice data stored in the buffer memory.

19. (original):

The network device of claim 18, wherein the descriptor fields include a memory pointer field, status field, mask field, or a data length field.

20. (cancelled)

21. (original):

A medium storing instructions, the instructions to be processed by a processing unit to perform an operation comprising:

receiving a network packet including voice data;

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storing the voice data;

sending a voice packet related to the voice data to a line card without duplication, the voice packet including descriptor fields for multicasting the voice data; and

selectively multicasting the stored voice data to a plurality of user devices based on the descriptor fields in the voice packet.

22. (original):

The medium of claim 21, further providing instructions to be processed by the processing unit to perform an operation comprising:

receiving an Internet Protocol (IP) packet having the voice data.

23. (original):

The medium of claim 21, further providing instructions to be processed by the processing unit to perform an operation comprising:

sending the voice packet including a memory pointer field, status field, mask field, or a data length field.

24. (original):

The medium of claim 23, further providing instructions to be processed by the processing unit to perform an operation comprising:

selectively multicasting the voice data using a multicast hardware accelerator to send the voice data to selected user devices based on the mask field.

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25. (original):

The medium of claim 24, further providing instructions to be processed by the processing unit to perform an operation comprising:

selectively multicasting the voice data using a field programmable gate array (FPGA) device as the multicast hardware accclerator.